

Specification

Mainframe	DTG5078	DTG5274 / DTG5334
Maximum Data Rate	750 Mb/s	2.7 Gb/s (DTG5274) 3.35 Gb/s (DTG5334)
Number of card slot	A, B, C, D, E, F, G, H Eight slots	A, B, C, D Four slots
Delay control	1 ps resolution (All channel)	0.2 ps resolution (All channel)
Width control	5 ps resolution (All channel in slot A to D)	5 ps resolution (All channel)
Master slave operation	Up to 3 boxes	Up to 2 boxes

Output Module	DTGM10	DTGM20	DTGM21	DTGM30	DTGM31	DTGM32
Number of channel	4 or 2 ^{*1}	4 or 2 ^{*1}	4 or 2 ^{*1}	2	1	1
Amplitude (50 Ω) (1M Ω)	3.5 Vp-p ^{*2}	3.5 Vp-p ^{*3}	5.35 Vp-p ^{*4} 3.90 Vp-p ^{*5}	1.25 Vp-p	1.25 Vp-p	1.25 Vp-p
	10 Vp-p	7 Vp-p	7.81 Vp-p ^{*6}	2.5 Vp-p	2.5 Vp-p	2.5 Vp-p
Source Impedance (typical)	50 Ω	50 Ω	23 Ω or 50 Ω ^{*7}	50 Ω	50 Ω	50 Ω
Maximum output current	+/- 40 mA	+/- 80 mA	+/- 80 mA	+/- 80 mA	+/- 80 mA	+/- 80 mA
Rise /Fall Time (20~80%) at 1Vp-p into 50 Ω	< 540 ps (Variable)	< 340 ps (Variable)	< 350 ps	< 110 ps	< 110 ps	< 110 ps
Inhibit Input	-	-	Yes	-	-	-
External Jitter Input	-	-	-	-	Yes	Yes

*1. Ch3 and Ch4 are not available in the DTG5274 or the DTG5334.

*2. This value is limited by Maximum Output (+/-40mA Max.)

*3. This value is limited by Maximum Output (+/-80mA Max.)

*4. Source impedance is 23 Ω

*5. Source impedance is 50 Ω

*6. Recommended source impedance is 50 Ω

*7. This is selectable by moving the conductor in the box.

NOTE When the module(DTGM21, DTGM31 and DTGM32) is installed in the DTG5078 or the DTG5274, applies to firmware version I.xx. You have to upgrade the OS by using attached the Windows2000 Recovery Disk. Please refer to Appendix B "System Recovery" in User Manual.
